

B. Sc. Second Semester

Subject: ELECTRONICS

Course: ELE-202 Paper VI

(Effective from June 2009)

Title: DIGITAL ELECTRONICS -II

Chapter 1: 1. Flip-Flops

1. In a sequential circuit, the output depends on
 - a) Present inputs
 - b) Past inputs
 - c) Present and past inputs
 - d) None of the above
2. A simple flip-flop is a
 - a) 1 bit storage cell
 - b) 2 bit storage cell
 - c) 3bit storage cell
 - d) 4 bit storage cell
3. In an RS flip flop, when $R=S=1$, the output is
 - a) 1
 - b) Infinite
 - c) 0
 - d) Invalid
4. In an RS latch, the output is set to high, when
 - a) R is high, S is low
 - b) S is high, R is low
 - c) Both low
 - d) Both high
5. In an RS latch, the output is reset to low, when
 - a) R is high, S is low
 - b) S is high, R is low
 - c) Both low
 - d) Both high
6. In a D flip-flop, after the clock, the output is
 - a) 0
 - b) = D
 - c) 1
 - d) None of the above
7. In a JK flip-flop, is the inactive state.
 - a) $J=K=0$
 - b) $J=K=1$
 - c) $J=K$
 - d) $J \neq K$

Chapter 2: Counters